EREC G5 Stage 2 Sub-group

Meeting No. 7

To be held at William Gilbert Meeting Room, REEC Building, Sir William Siemens House, Princess Road, Manchester, M20 2UR

On Wednesday 11th January 2017 <u>10:00</u>-<u>15:00</u>

<u>Agenda</u>

| 1. | Welcome & Fire Procedure | SPS/AS | 10:00 |
|-----|---|--------|-------|
| 2. | Agree Notes of Previous Meeting | All | |
| 3. | Actions from Meeting 5 | | |
| 3.1 | Definition of Converter Types | AS | |
| 3.2 | Harmonic Impedance of LV Networks | SPS | |
| 4 | Stage 1 & 2 Draft | | |
| 4.1 | Impact of Siemens Emission Data | AS/FG | |
| 4.2 | Stage 1 & 2 Draft – Update | SPS | |
| 4.3 | Stage 1A - Revised Fig 2, Text & Supporting Spreadsheet | SPS | |
| 4.4 | Stage 1B – Revised Fig 3, Table 2, Table 3, Text & Supporting Spreadsheet | SPS | |
| 4.5 | Stage 2C – Impact of 77A/926/CDV Compatibility Levels beyond 40th | FGh | |
| 4.6 | Stage 1 & 2 Draft Worked Examples - Update | SPS | |
| 4.7 | Stage 2C Thevenin Equivalent Example | FG | |
| 5 | Agree Further Work | All | |
| 6 | AOB | All | |
| 7 | Future meetings | All | |
| | • Dates | | |
| | Agenda items | | |
| 8 | Close | SPS | 15:00 |