EREC G5 Stage 2 Sub-group

Meeting No. 8

To be held at William Gilbert Meeting Room, REEC Building, Sir William Siemens House, Princess Road, Manchester, M20 2UR

On Wednesday 22nd February 2017 10:00-15:00

<u>Agenda</u>

1.	Welcome & Fire Procedure	SPS/AS	10:00
2.	Agree Notes of Previous Meeting	All	
3.	Actions from Meeting 7		
3.1	Definition of Converter Types	AS	
3.2	Harmonic Impedance of LV Networks	SPS	
4	Stage 1 & 2 Draft		
4.1	Stage 1 & 2 Draft – Review Comments received including ENW comments	SPS	
4.2	Stage 2C – Impact of 77A/926/CDV Compatibility Levels beyond 40th	FGh	
4.3	Stage 1 & 2 Draft Worked Examples - Update	SPS	
4.4	Stage 2C Thevenin Equivalent Example	FG	
4.5	Documentation of Background to Stage 1 & 2 Draft	SPS	
5	Agree Further Work	All	
6	AOB	All	
7	Future meetings	All	
	• Dates		
	Agenda items		
8	Close	SPS	15:00